

Fig. 1 (prior art)

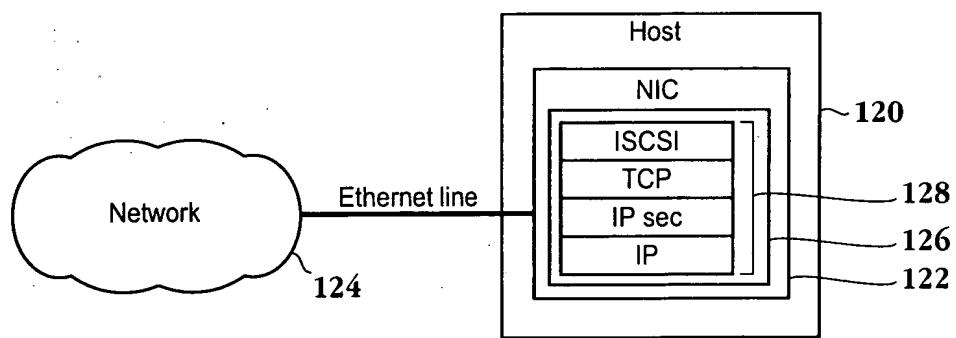
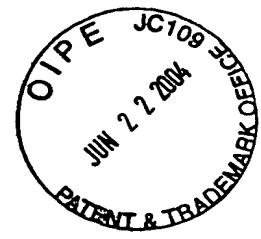


Fig. 2

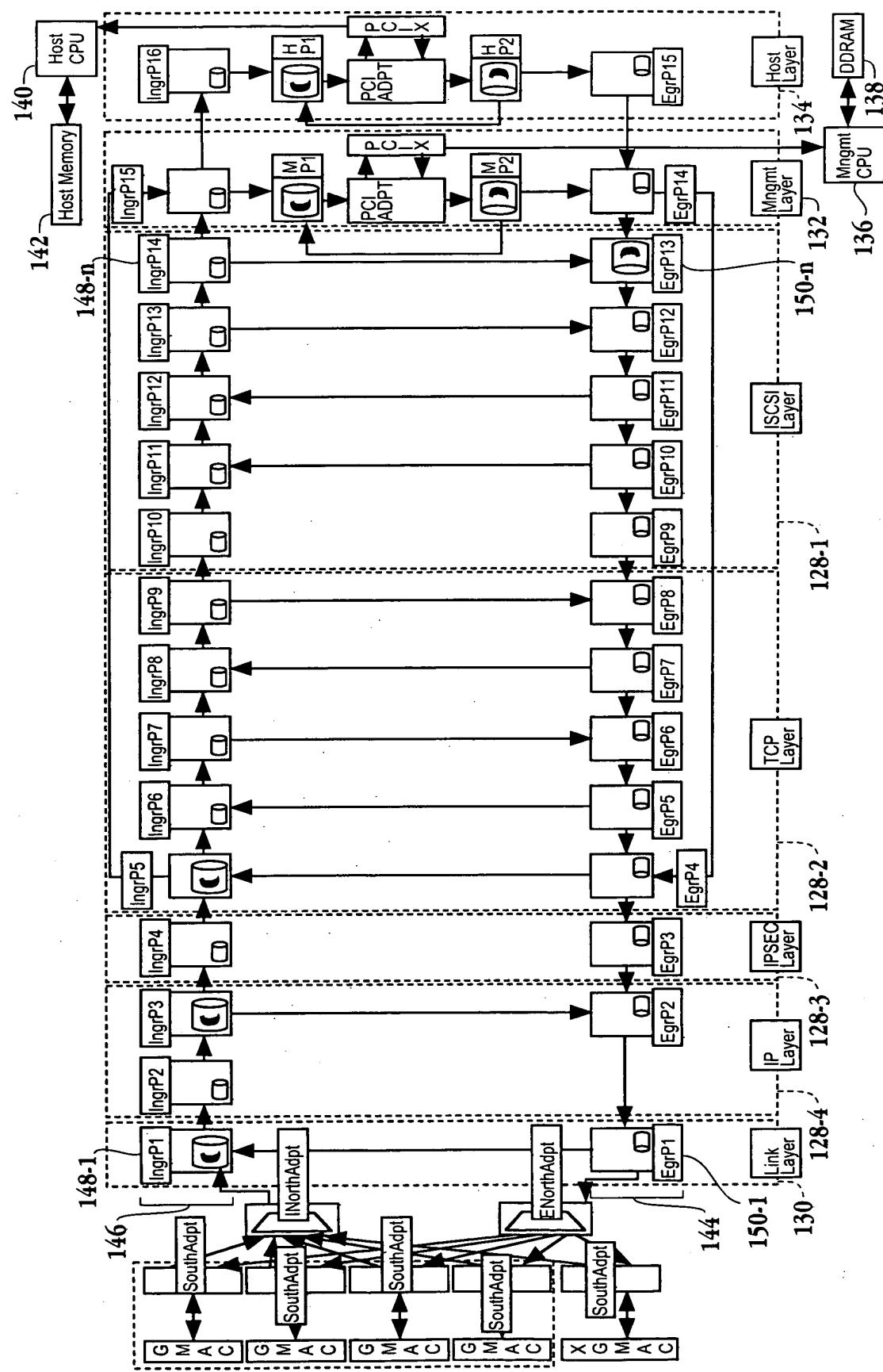
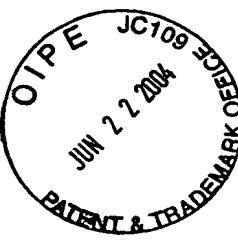


Fig. 3

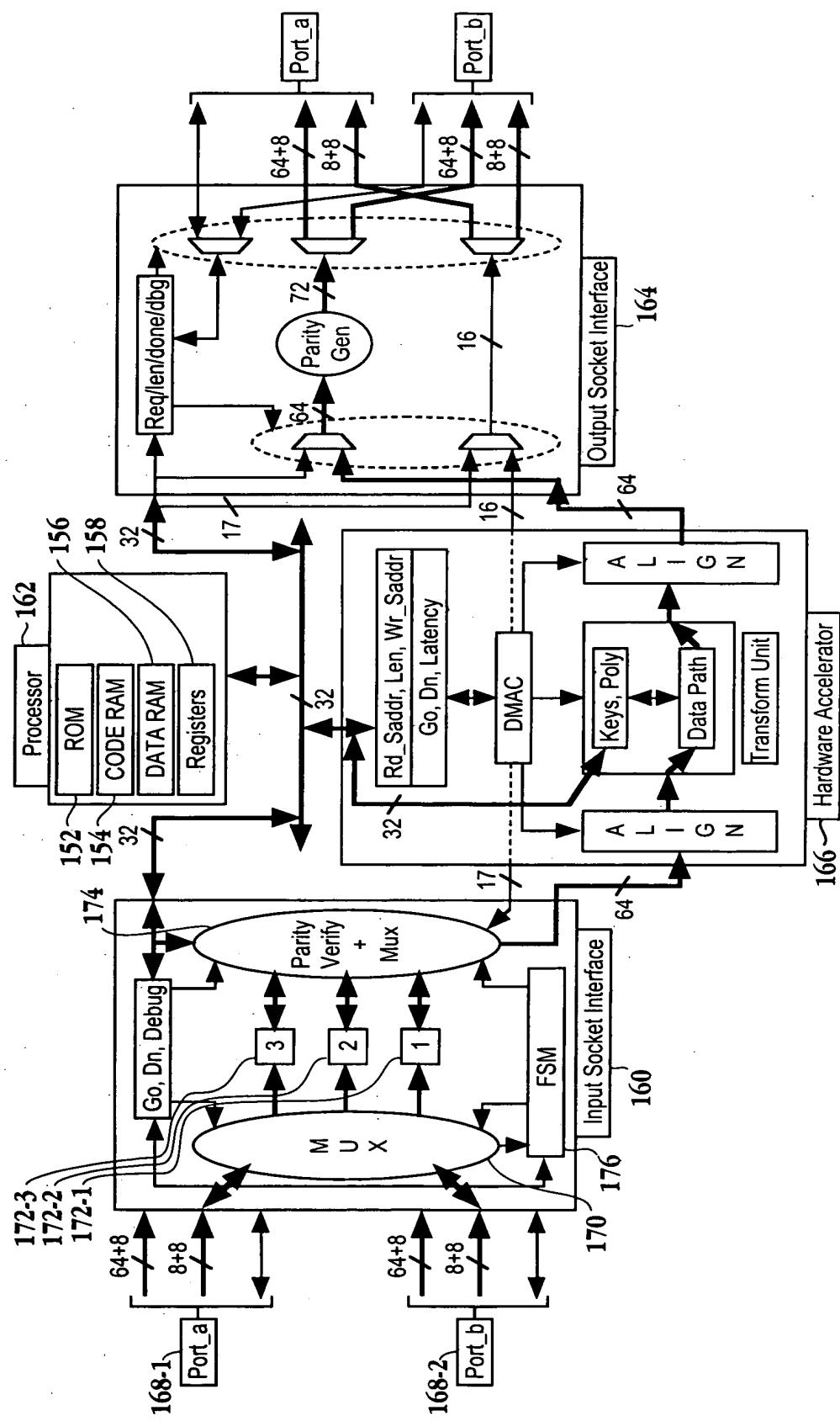
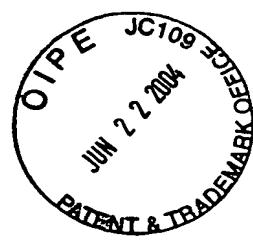


Fig. 4

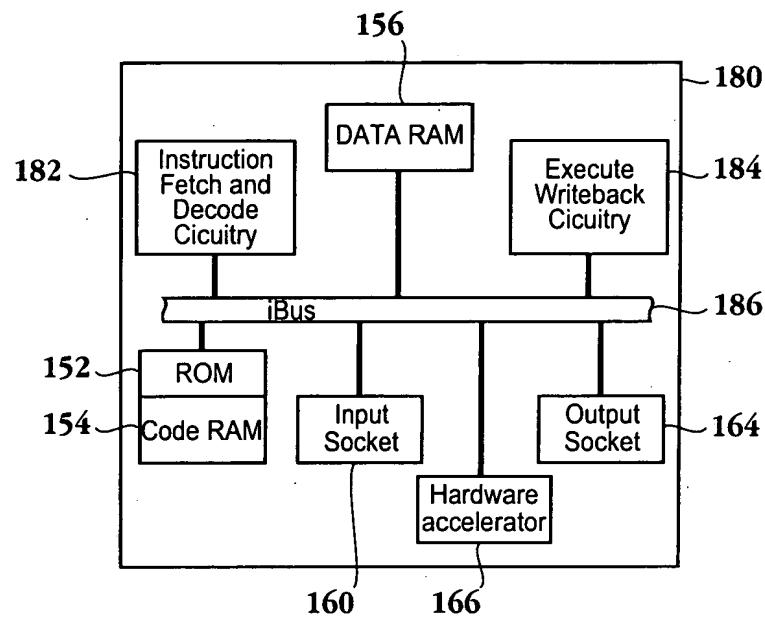


Fig. 5

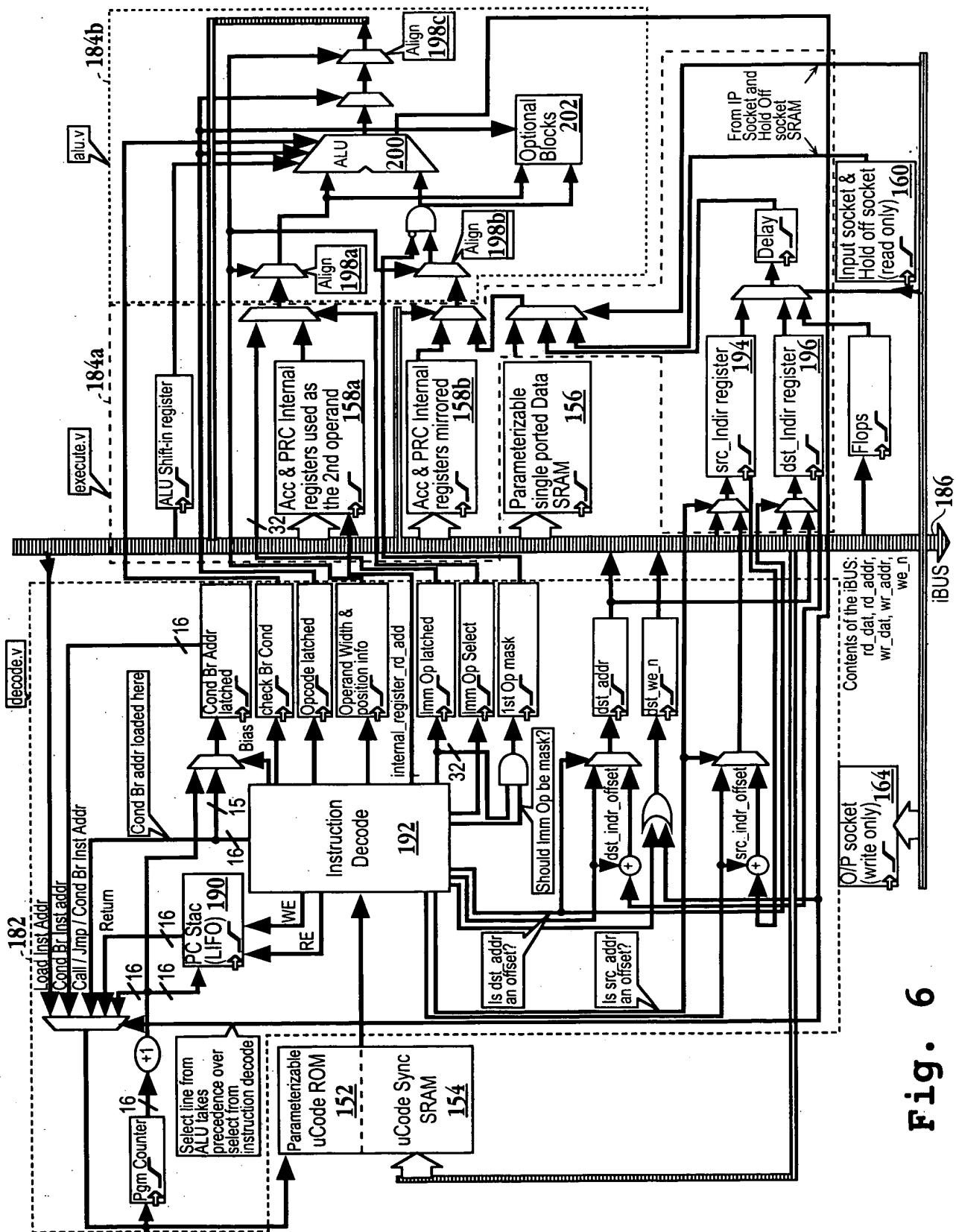
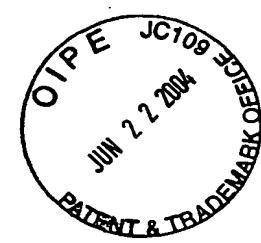


Fig. 6



Title: Method and Apparatus For Aligning
Operands For A Processor
Application No: 10/726,427
File No: ADAPP221
Inventor: MUKUND et al.
Page 7 of 9

I - instruction fetch and decode
II - execute and write back

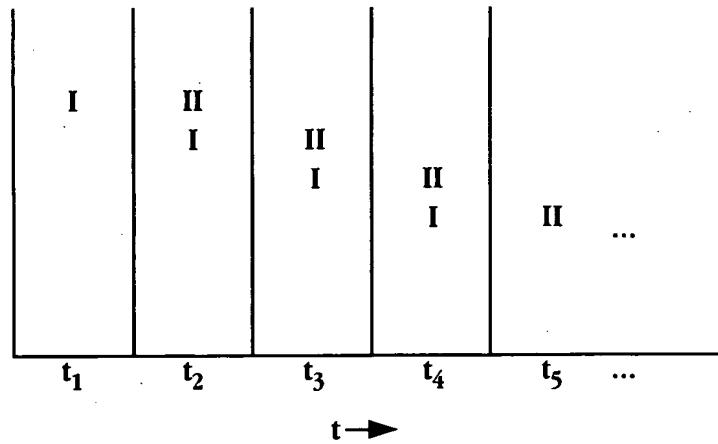


Fig. 7

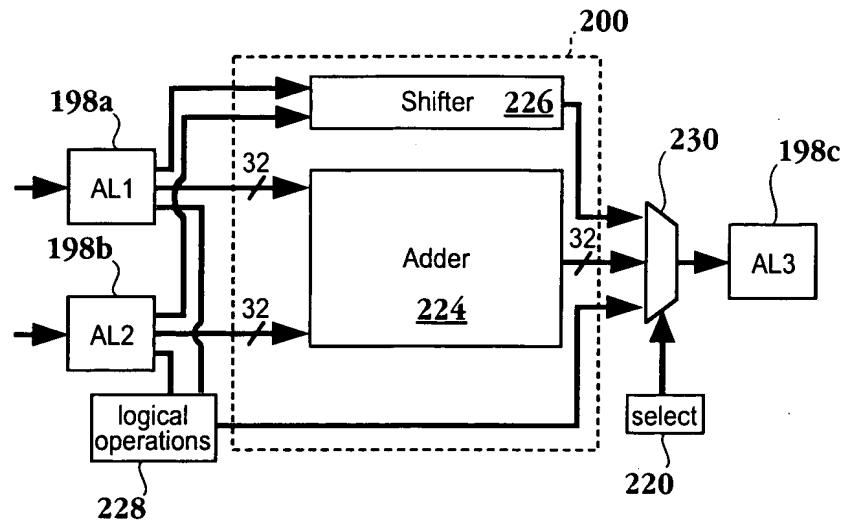


Fig. 8

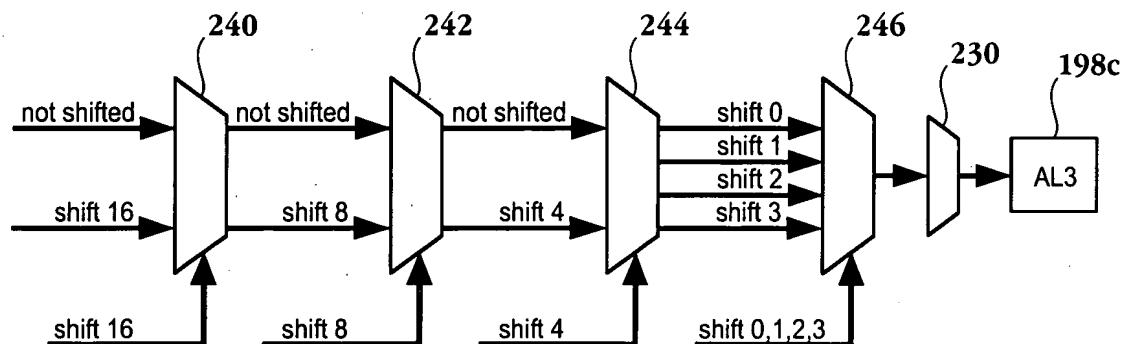


Fig. 9

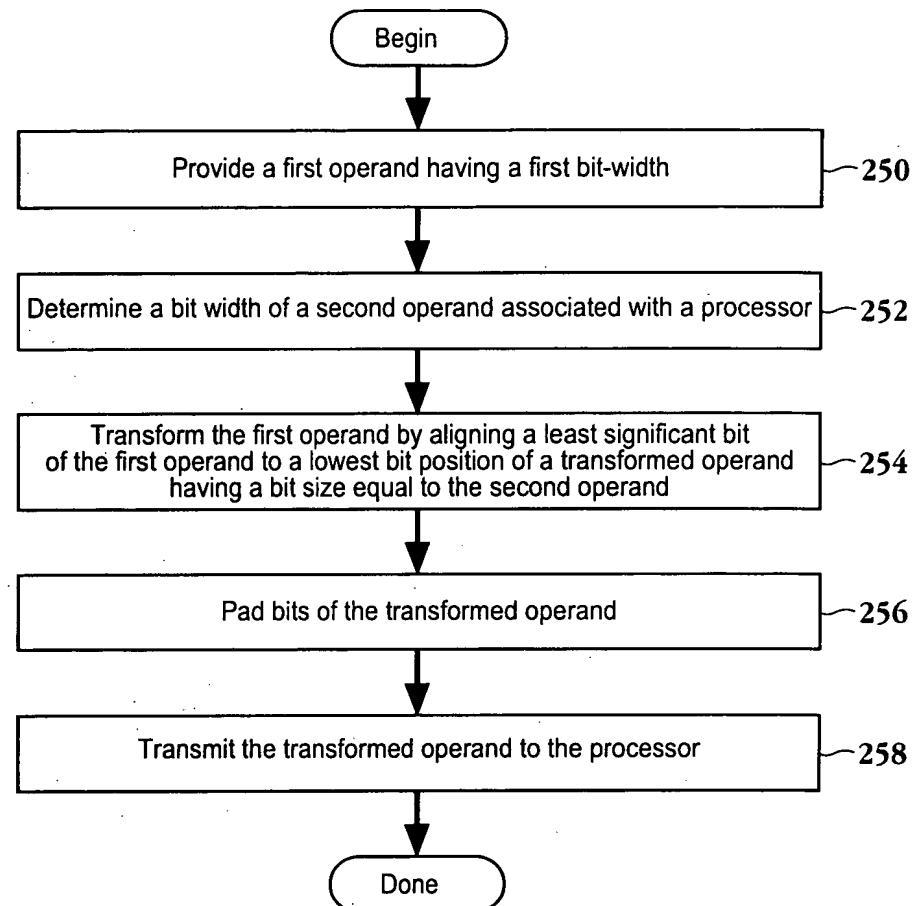


Fig. 10